

PATENT NUMBER

**U.S. UTILITY Patent Application**

<b>KIV</b> <b>SCANNED</b>	<b>O.I.P.E.</b> <b>QA</b>	<b>PATENT DATE</b> <b>Bonura</b>
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APPLICATION NO 09/583057	CONT/PRIOR	CLASS 71914	SUBCLASS 30	ART UNIT 2763 2184	EXAMINER Bonura
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## APPLICANTS

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**TITLE**

Control method and apparatus for testing of multiple processor integrated circuits and other digital systems

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

[illegible]☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____				<b>ISSUE FEE</b>	
	_____ (Primary Examiner) _____ (Date)	Amount Due	Date Paid		
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			<b>ISSUE BATCH NUMBER</b>	
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